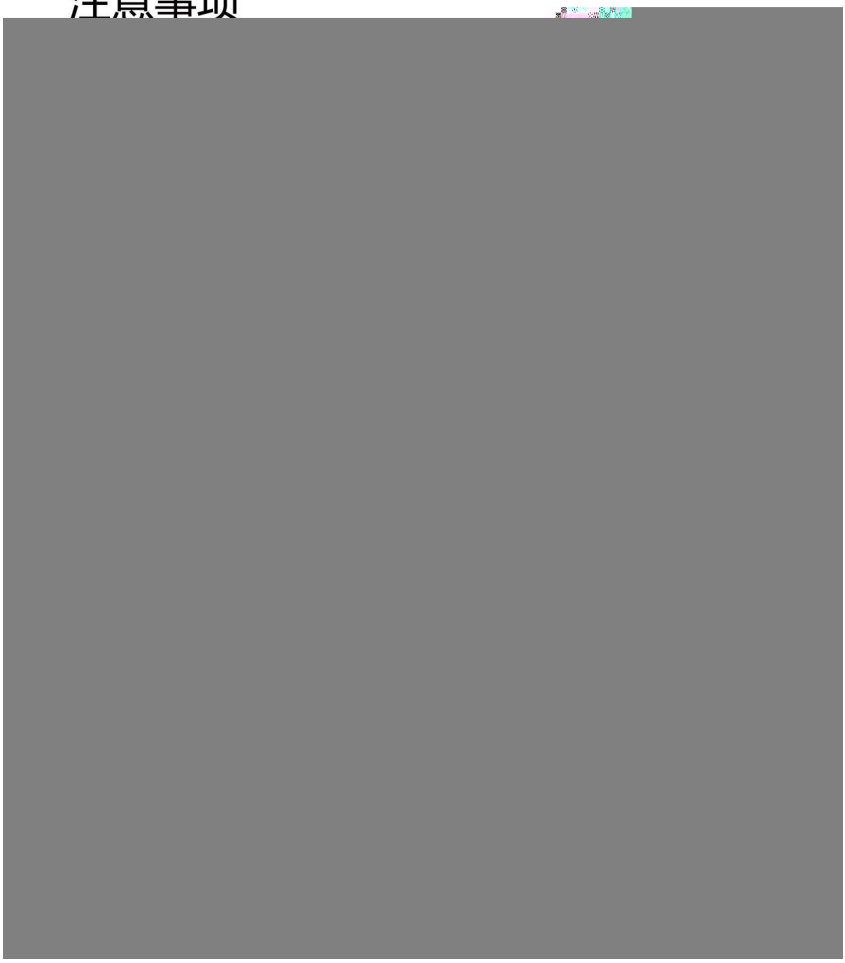


T 2.8 _V1.0

T-0S07

注意事項



1

1.1 1

1.2 1

2

7.4 20
7.5 20

IC

1.1

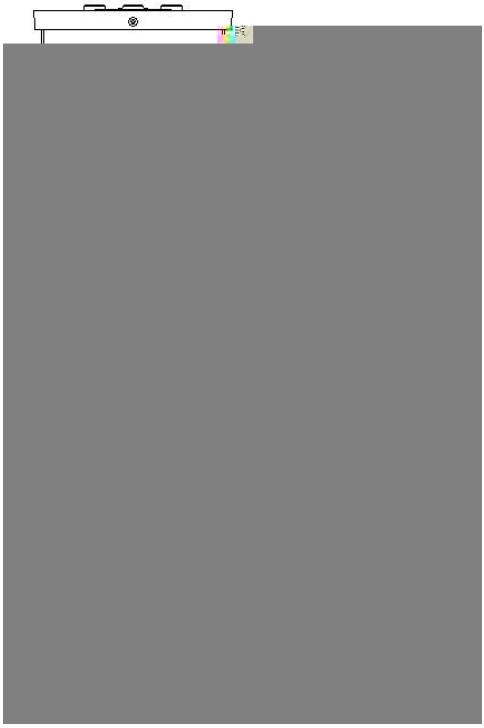
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1.2

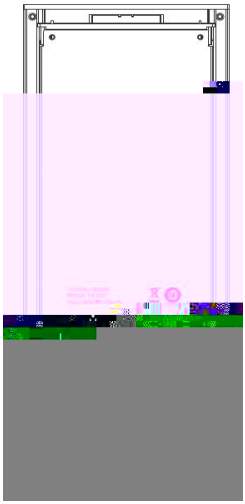
DC 24~32V	≤ 80mA DC 30V
-25 ~55	≤ 160mA DC 30V
-40 ~70	(W/H/D) 324×128×46mm

CMOS	700TVL
48°	0.1 lux

2.2



2.3



1 2 TX+ TX- 3 6 RX+ RX-

BUS-A BUS-B

NO COM NC / /

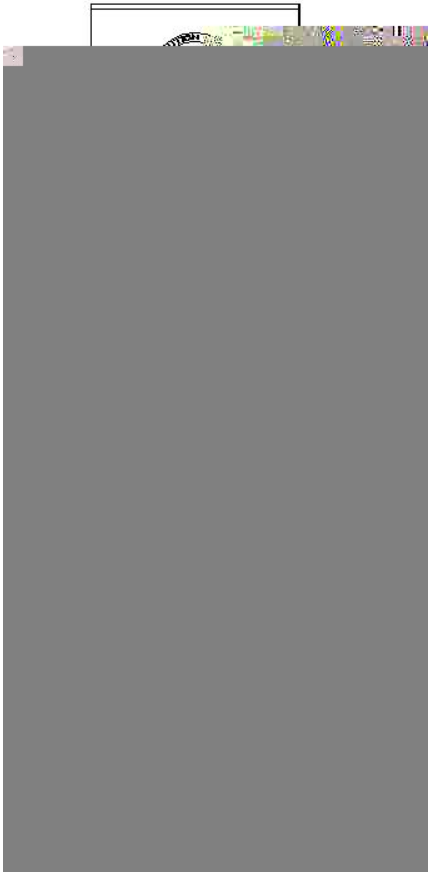
GS

DOOR SW

GND

24V ∩

3.1



1.6

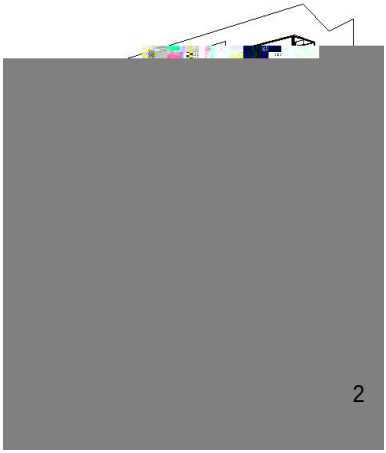
3.2

2

W/H/D 1



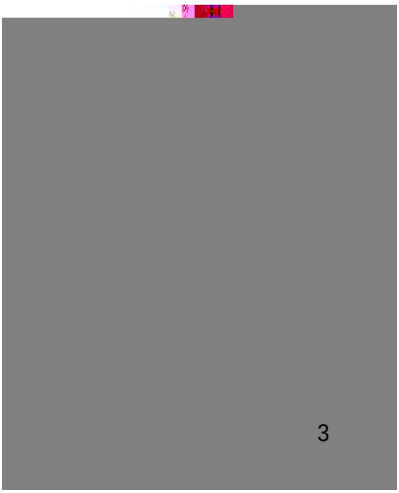
1



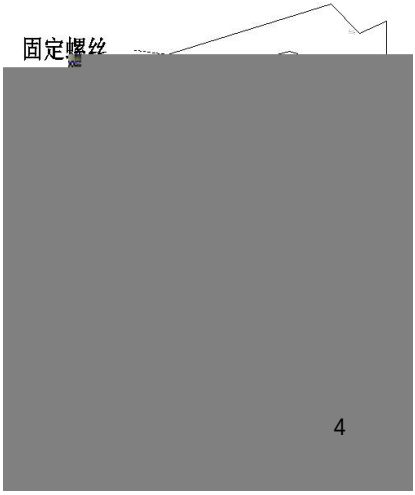
2

3

4



3



4

3.3

3.4









GS

4.1

“ # ”

“ ”



4.2

“ ↑ ↓ ” PC



“ ↑ ↓ ” “ # ”

4.3

4.4

1)

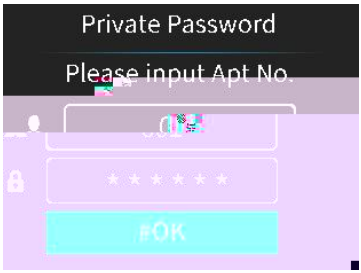
2)

3)

4)

5)

“ *# ”



“

6.1

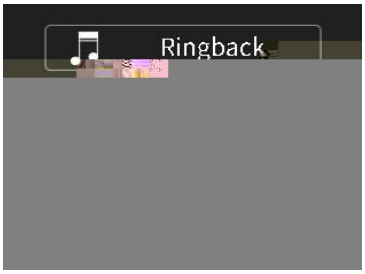
“ ↑ ↓ ” “ * ”

“ # ”



6.2

“ # ” “ * ”



6.3

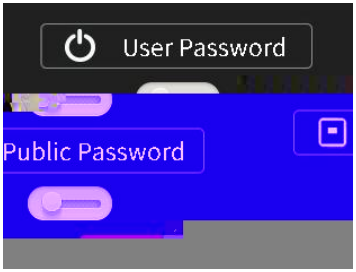
“ ↑ ↓ ” “ # ”



6.4

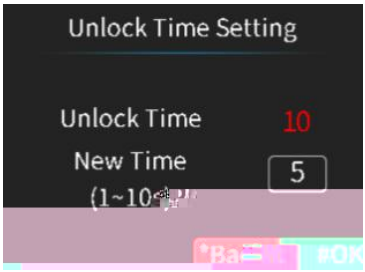
“ ↑ ↓ ”

“ # ” “ * ”

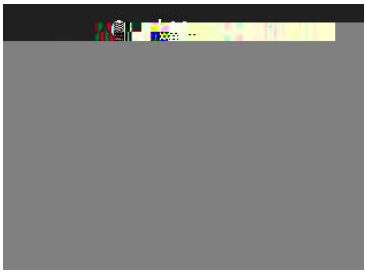


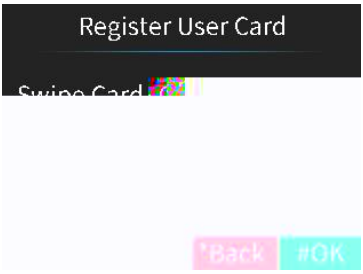
6.5

“ ” “ * ” “ # ”


Unlock Time Setting
Unlock Time
10
New Time
5
#OK

6.6



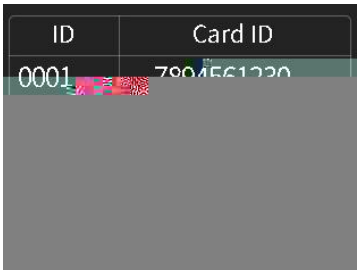

Register User Card
*Back
#OK

“ # ”

“ * ” “ * ”

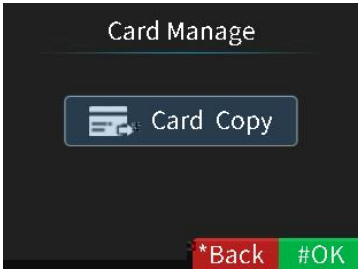
2000

“ ↑ ↓ ”



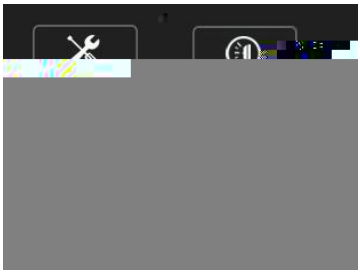
“ # ”

“ * ”



PC **PC**

“ ↑ ↓ ”



1



2

7.1

1~32 “ # ”



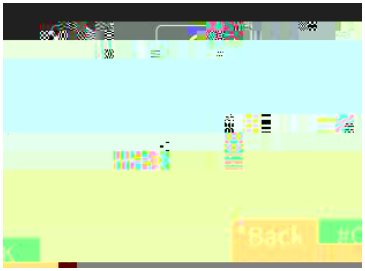
7.2

“ # ”

120S



7.3



1 *2*

30S

30S

[redacted]

7.4